

FIG. 1

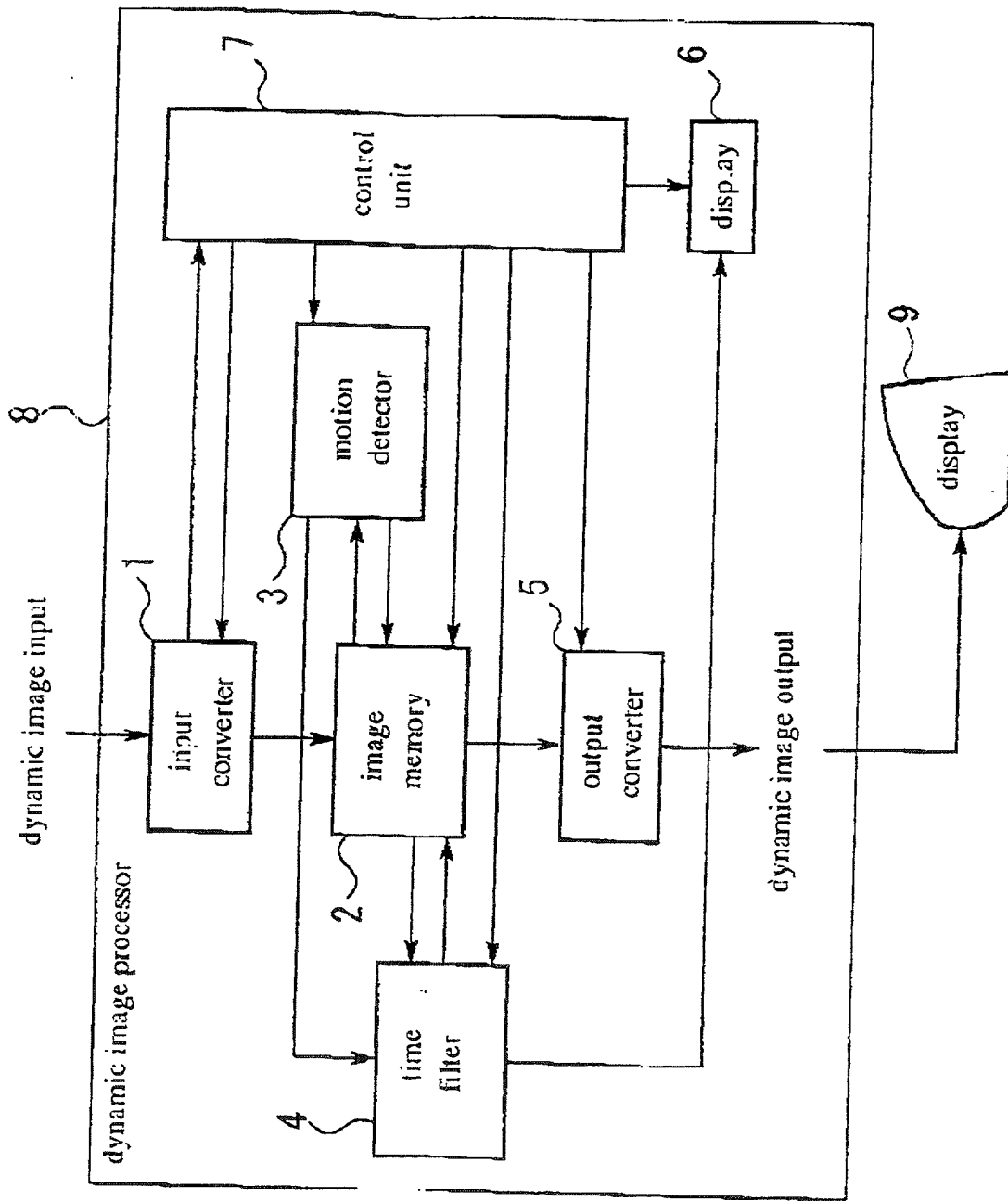


FIG. 2

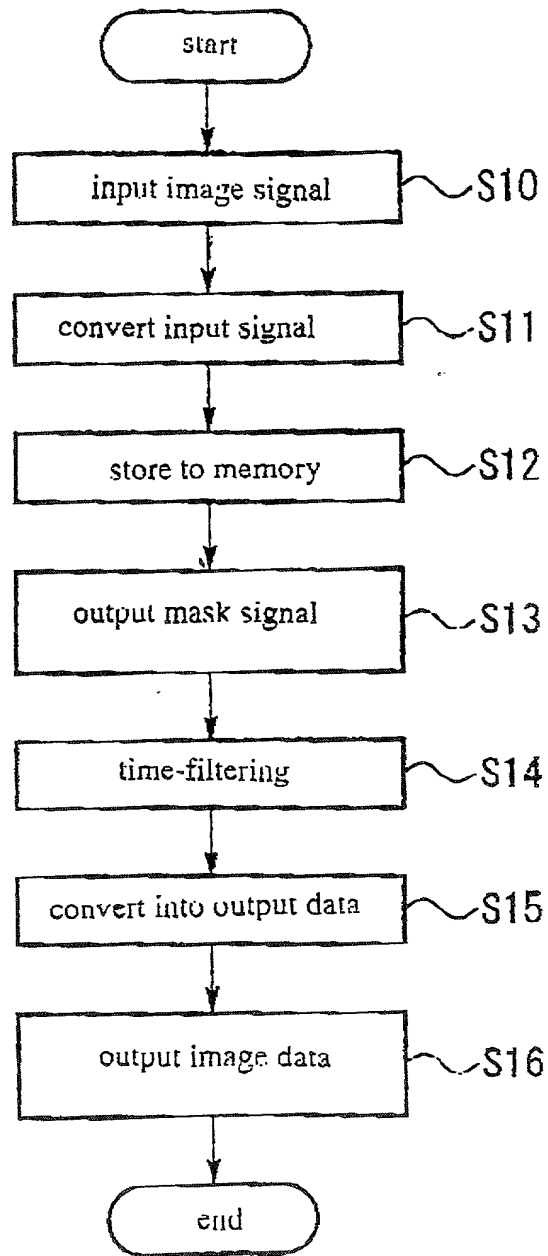


FIG. 3

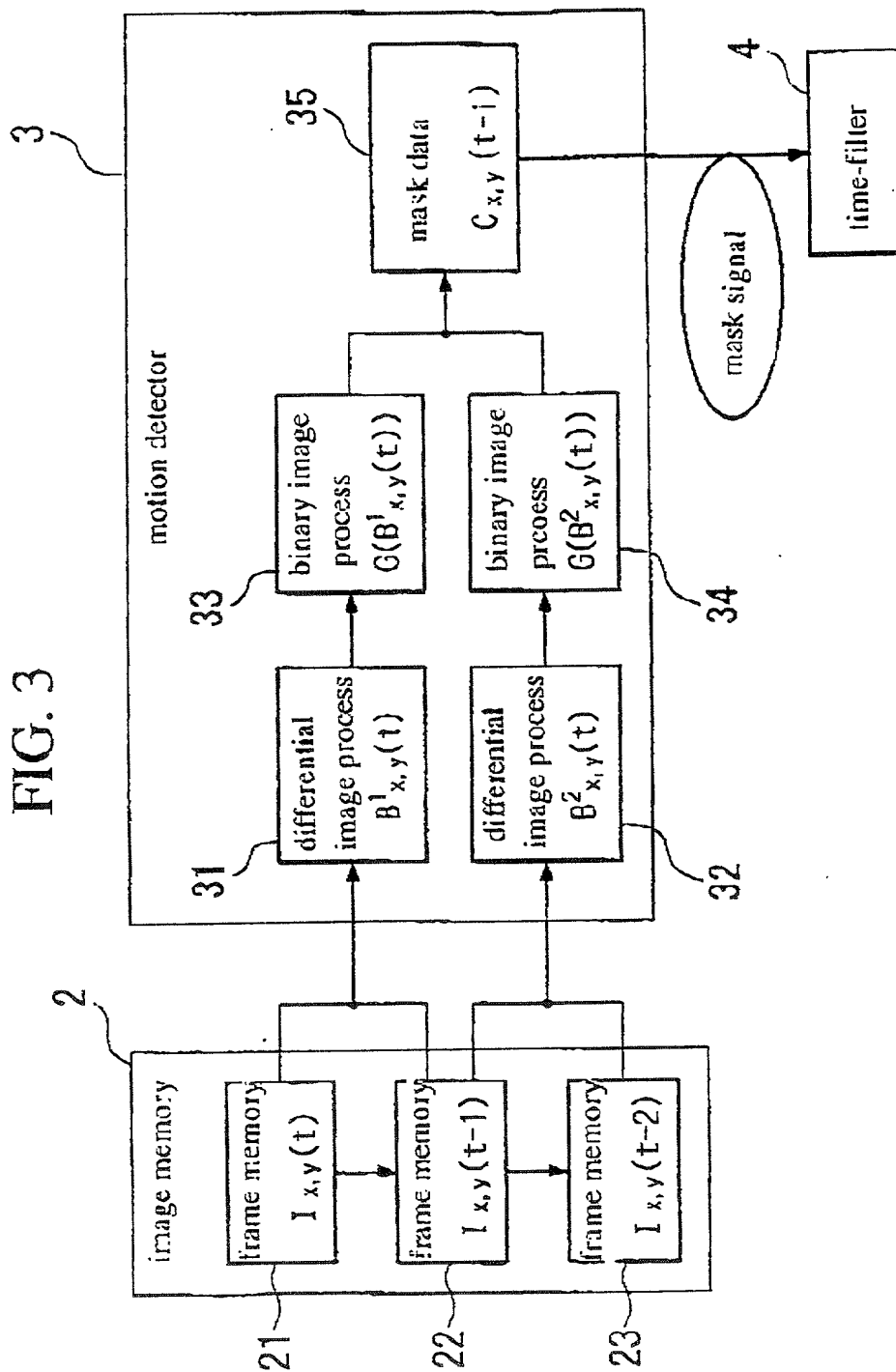


FIG. 4

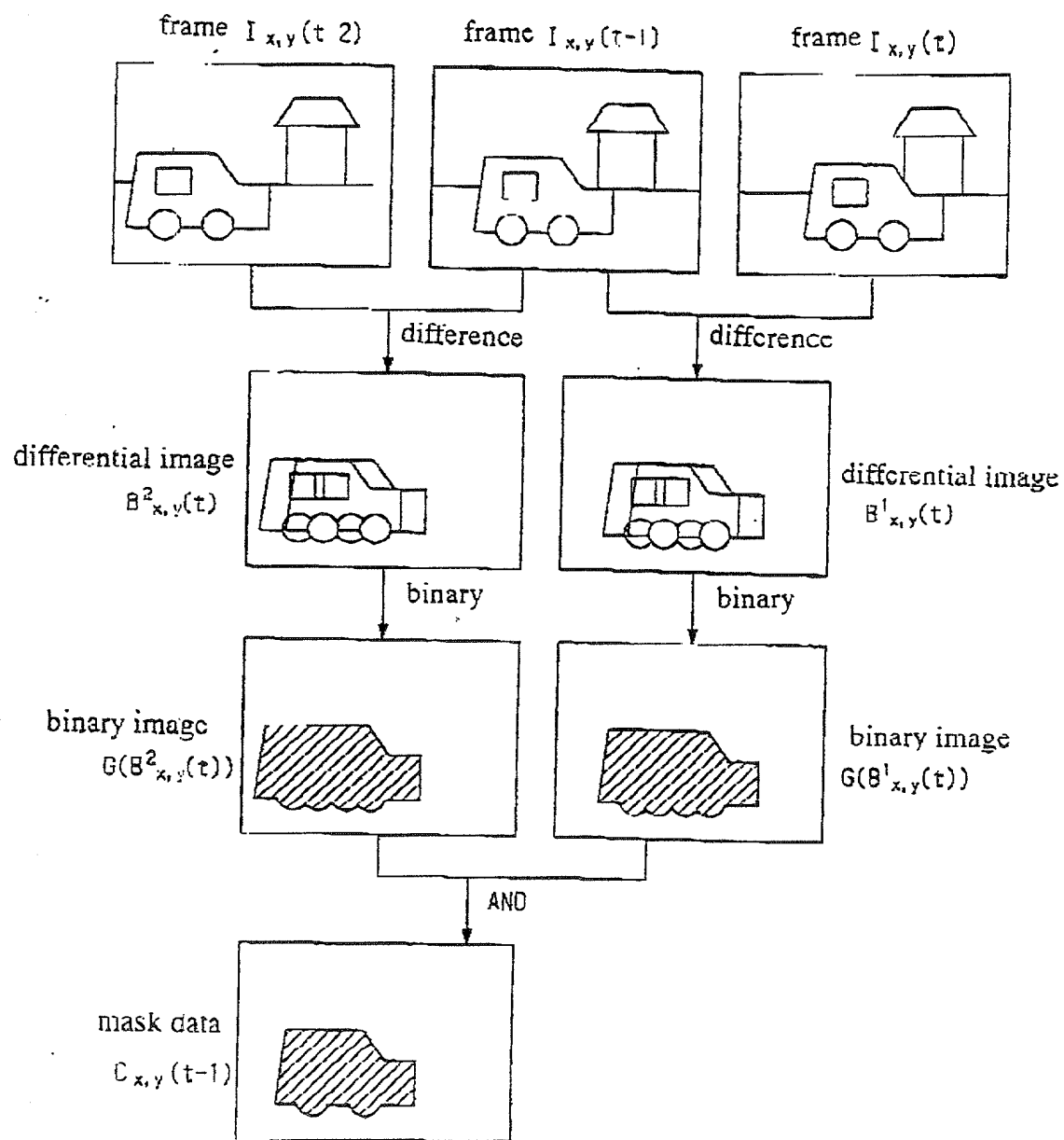


FIG. 5

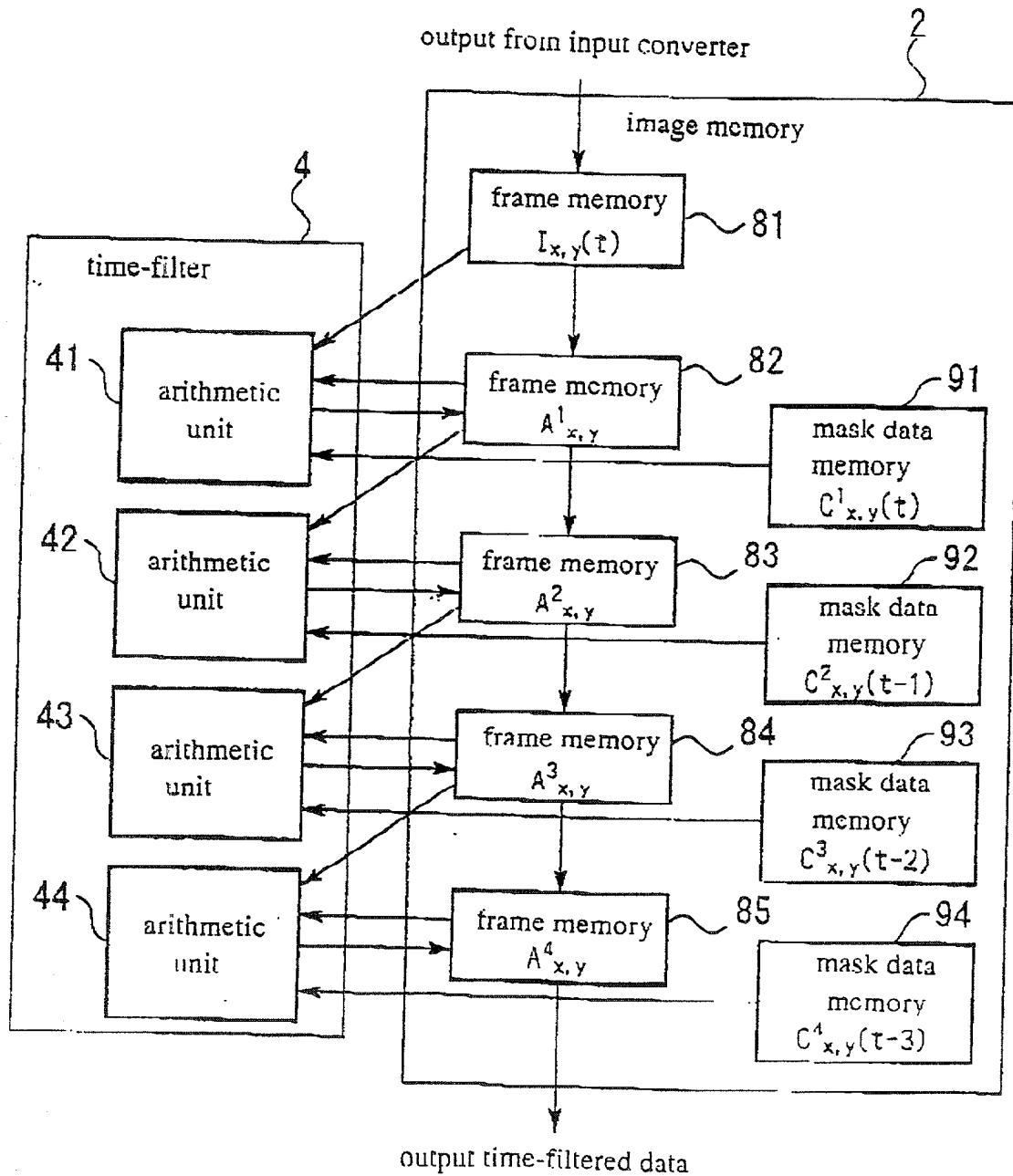


FIG. 6

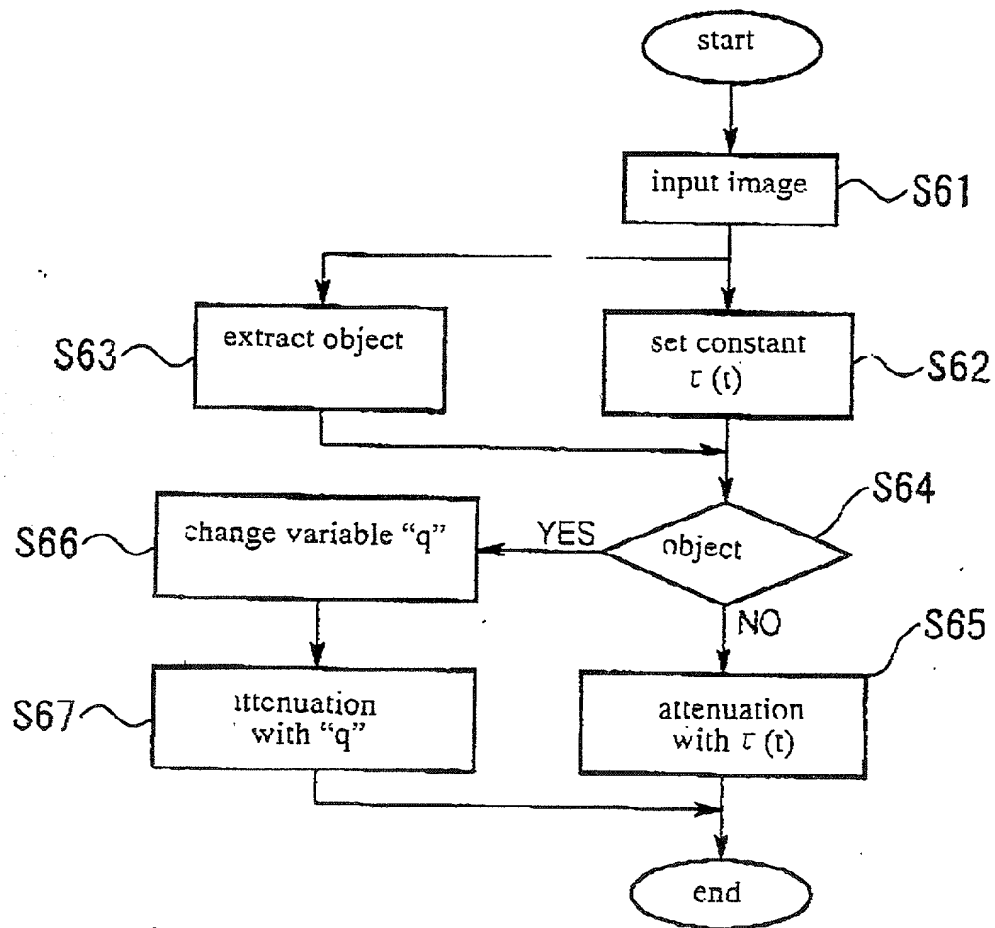
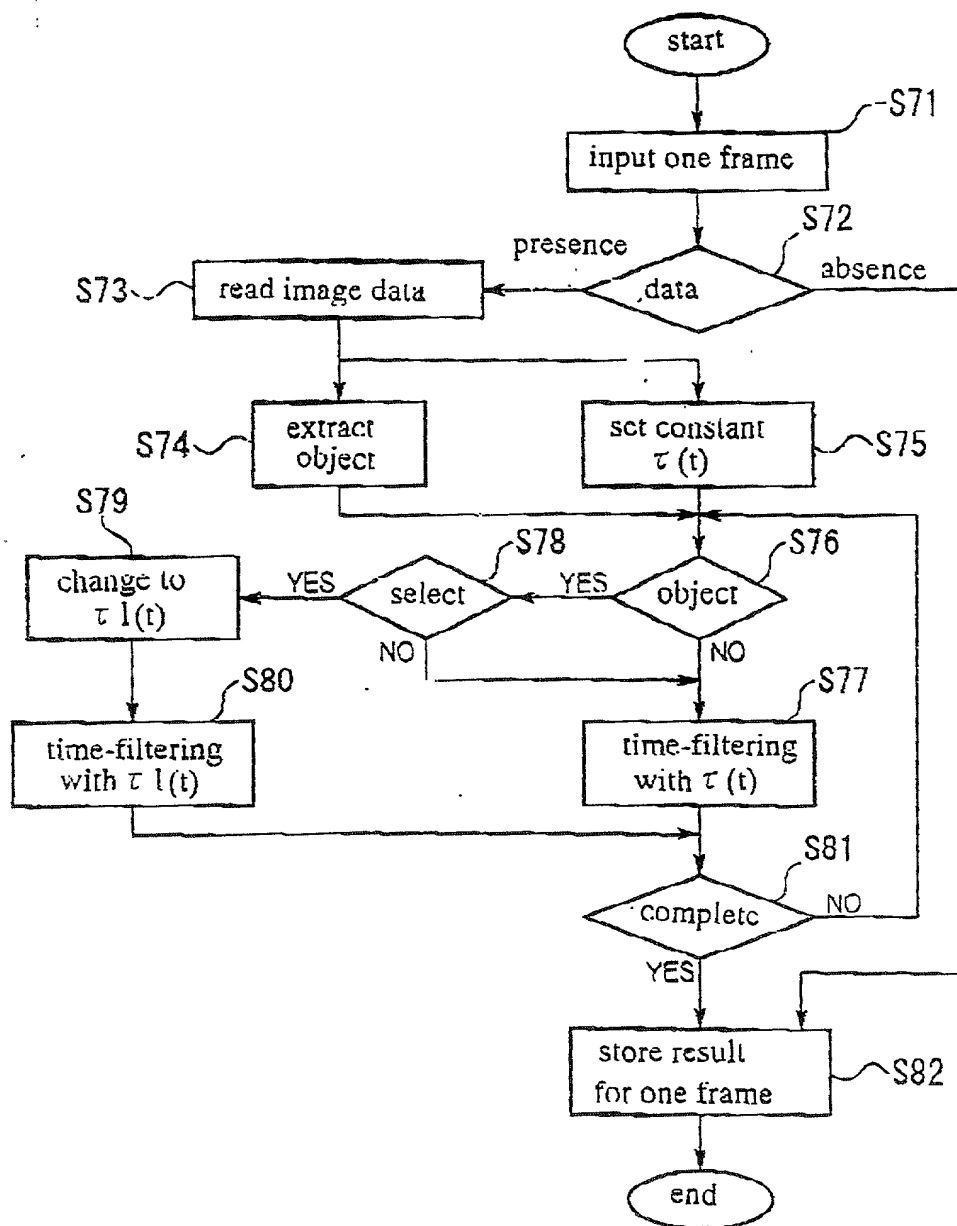


FIG. 7



The diagram illustrates a dynamic image processor 8'. It features a central image memory 2 connected to several components: a time filter 4, an input converter 1, an output converter 5, and a motion detector 7. A control unit 6 is also connected to the image memory 2, the motion detector 7, and the output converter 5. The input path starts with a dynamic image input 8' entering a compression decoder 101, which then feeds into the input converter 1. The output path shows the output converter 5 leading to a dynamic image output. Bidirectional arrows indicate data flow between the image memory 2 and the time filter 4, input converter 1, output converter 5, and motion detector 7. The control unit 6 has bidirectional connections with the motion detector 7 and the output converter 5, and a unidirectional connection to the image memory 2.